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APPLICANTS

Toshihiko Uno, Osaka, JAPAN;

** CONTINUING DATA *****

This application is a DIV of 10/307,361 12/02/2002 ABN *de*

** FOREIGN APPLICATIONS *****

JAPAN 2001-368089 12/03/2001 *de*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 11	TOTAL CLAIMS 3	INDEPENDENT CLAIMS 1
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged Examiner's Signature <i>de</i> Initials				

ADDRESS

McDermott, Will & Emery
 600 13th Street, N.W.
 Washington, DC
 20005-3096

TITLE

Method of manufacturing a semiconductor device having a high breakdown voltage and low on-resistance

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